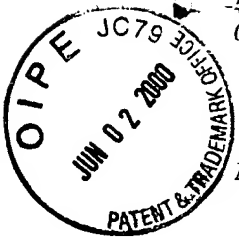


Match & Return

IDT-1548
09/185,057



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chuen-Der Lien et al.

Assignee: Integrated Device Technology, Inc.

Title: LOW-POWER CONTENT ADDRESSABLE MEMORY CELL

Serial No.: 09/185,057

Filed: 11/02/1998

Examiner: H. Ho

Group Art Unit: 2818

Attorney Docket No.: IDT-1548

11/03
Reilly
3/12
J. MacKay
8-9-00

San Jose, CA
May 30, 2000

BOX ISSUE FEE
Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AFTER ALLOWANCE

(37 C.F.R. § 1.312)

Sir:

Applicants respectfully submit the following amendments in response to the Notice of Allowability mailed on May 11, 2000.

IN THE CLAIMS

Please amend Claims 5 and 21 as follows:

5. (Amended) The CAM cell of Claim 4, wherein a drain region of the first transistor is coupled to a drain region of the second [first] transistor at a first node.

21. (Amended) A method of operating a content addressable memory (CAM) cell that includes a static random access (SRAM) cell, the method comprising [the steps of]:

operating the SRAM cell in response to a V_{cc} supply voltage, the SRAM cell storing a data value;

The entry is approved under 0.3311
8/15/00

B1

B2